Contact assignment on *CompactPCI*® system position (J1/P1)

	a	b	c	d	e	
25	+5 V	REQ64#	ENUM#	+3,3 V	+5 V	25
24	AD[1]	+5 V	V(I/O)	AD[0]	ACK64#	24
23	+3,3 V	AD[4]	AD[3]	+5 V	AD[2]	23
22	AD[7]	GND	+3,3 V	AD[6]	AD[5]	22
21	+3,3 V	AD[9]	AD[8]	M66EN	C/BE[0]#	21
20	AD[12]	GND	V(I/O)	AD[11]	AD[10]	20
19	+3,3 V	AD[15]	AD[14]	GND	AD[13]	19
18	SERR#	GND	+3,3 V	PAR	C/BE[1]#	18
17	+3,3 V	SDONE	SBO#	GND	PERR#	17
16	DEVSEL#	GND	V(I/O)	STOP#	LOCK#	16
15	+3,3 V	FRAME#	IRDY#	GND	TRDY#	15
14						14
13			Key Area			13
12						12
11	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	11
10	AD[21]	GND	+3,3 V	AD[20]	AD[19]	10
9	C/BE[3]#	IDSEL	AD[23]	GND	AD[22]	9
8	AD[26]	GND	V(I/O)	AD[25]	AD[24]	8
7	AD[30]	AD[29]	AD[28]	GND	AD[27]	7
6	REQ#	GND	+3,3 V	CLK	AD[31]	6
5	Bus Reserved	Bus Reserved	RST#	GND	GNT#	5
4	Bus Reserved	GND	V(I/O)	INTP	INTS	4
3	INTA#	INTB#	INTC#	+5 V	INTD#	3
2	TCK	+5 V	TMS	TDO	TDI	2
1	+5 V	-12 V	TRST#	+12 V	+5 V	1
	a	b	c	d	e	

Contact assignment on *CompactPCI*® system position (J2/P2)

	a	b	c	d	e	
22	GA4	GA3	GA2	GA1	GA0	22
21	CLK6	GND	Reserved	Reserved	Reserved	21
20	CLK5	GND	Reserved	GND	Reserved	20
19	GND	GND	Reserved	Reserved	Reserved	19
18	Bus Reserved	Bus Reserved	Bus Reserved	GND	Bus Reserved	18
17	Bus Reserved	GND	PRST#	REQ6#	GNT6#	17
16	Bus Reserved	Bus Reserved	DEG#	GND	Bus Reserved	16
15	Bus Reserved	GND	FAL#	REQ5#	GNT5#	15
14	AD[35]	AD[34]	AD[33]	GND	AD[32]	14
13	AD[38]	GND	V(I/O)	AD[37]	AD[36]	13
12	AD[42]	AD[41]	AD[40]	GND	AD[39]	12
11	AD[45]	GND	V(I/O)	AD[44]	AD[43]	11
10	AD[49]	AD[48]	AD[47]	GND	AD[46]	10
9	AD[52]	GND	V(I/O)	AD[51]	AD[50]	9
8	AD[56]	AD[55]	AD[54]	GND	AD[53]	8
7	AD[59]	GND	V(I/O)	AD[58]	AD[57]	7
6	AD[63]	AD[62]	AD[61]	GND	AD[60]	6
5	C/BE[5]#	GND	V(I/O)	C/BE[4]#	PAR64	5
4	V(I/O)	Bus Reserved	C/BE[7]#	GND	C/BE[6]#	4
3	CLK4	GND	GNT3#	REQ4#	GNT4#	3
2	CLK2	CLK3	SYSEN#	GNT2#	REQ3#	2
1	CLK1	GND	REQ1#	GNT1#	REQ2#	1
	a	b	c	d	e	

In mechanical terms J1/P1 is a 25x5 matrix of contacts. Three rows of 5 contacts (rows 12 - 14) are not used for electrical contacts. Instead, plastic keys of different orientation and configuration are used to key board locations as to system or peripheral slot, voltage options, etc.

J2/P2 is a shortened connector with only 22 rows of contacts instead of 25 rows for a standard size. HARTING now offers monolithic versions with J1/P1 and J2/P2 combined in one single connector.

This combination together with some space left on the card to fit into guide rails makes maximum use of the 100 mm rear edge of the 3U Eurocard.

On a 6U card this connector setup is repeated on J4/P4 and J5/P5.

The J3/P3 connector is a shortened version of the 2.0 mm connector with 19 rows of 5 signal contacts.

The size results from the height of a 6U board (233 mm) which is more than double the height of a 3U board.

All connectors used for *CompactPCI*® are based on a 7 column pitch. The inner 5 columns are used for logic signals and power. The outer columns on either side are reserved for shielding or ground.

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